# **HESION** | 禾信

### **Technical Specification V1.0** 2022.02

#### ZDR20-48BS3V3ST DC-DC Converters

Input 18V-75V, Output 3.3V/6A, 1in.×1in. Standard Size

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#### **Features**

- ◆ 1in.×1in. Compact Size (25.4 mm×25.4 mm ×10.5mm)
- ◆ 18 to 75 Vdc Wide Input Voltage Range
- ◆ Positive Logic Control (3.5V to 15V or floating Turn on)
- Output Voltage Adjust Range: ±10% of the rated output voltage
- Output Short-circuit Protection, automatic recovery
- ♦ High Efficiency up to 89% (48V, full load)
- ◆ 2250Vdc Isolation Voltage
- lacktriangle Operation Case Temperature -40  ${\mathcal C}$  to +105  ${\mathcal C}$
- ◆ Surface mount (SMT) or Through hole (TH)
- Applications: Instruments, vehicle-mounted equipment and other high-reliability fields, distributed power architecture, etc.





### **Ordering Information**

See Contents for individual product ordering numbers.

Suffix	Description	Ordering No.
	Positive Logic Control	ZDR20-48S3V3T
N	Negative Logic Control	ZD20-48S3V3NT
S	Surface mount (SMT)	ZDR20-48S3V3ST
NS	Negative Logic Control, Surface mount (SMT)	ZDR20-48S3V3NST

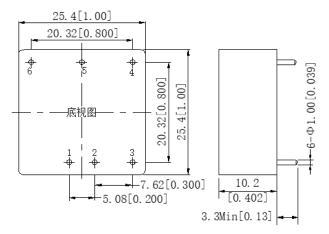
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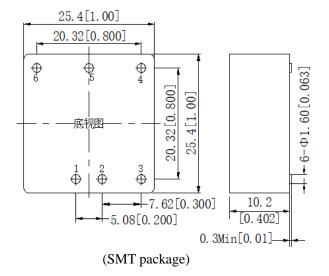
### ZDR20-48BS3V3ST DC-DC Converters

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## **Outline Diagram**



(TH package)



Pin	Symbol	Function		
1	+Vin	Positive Input		
2	2 –Vin Negative Input			
3	CNT	Remote Control Pin		
4	-Vo	Negative Output		
5	Trim	Output voltage adjust		
6	+Vo	Positive Output		

Case material:

Aluminum, black; Pin: copper with tin-cerium

Notes: all dimensions in mm(inches)
Tolerances:X.X±0.5mm(X.XX±0.02)
X.XX±0.25mm(X.XXX±0.010)

## **Specification**

Unless otherwise specified, all values are given at: 25°C, one standard atmosphere pressure, pure resistive load and basic connection.

Inp	ut	Symbol	Min	Тур	Max	Unit	Conditions
Input V	oltage	$V_{in}$	18	24/48	75	Vdc	_
Input Volt	age max.	Vin max	-	-	100	Vdc	100ms
Input C	urrent	I <sub>in</sub>	_	_	1.3	A	$V_{in}=18Vdc, I_O=\pm 1.5A$
No-load inp	out current	I <sub>in nl</sub>	_	_	40	mA	V <sub>in</sub> =9Vdc, I <sub>O</sub> =0A
D :::	ON	_	3.5	_	15	V	Refer to $-V_{in}$ Also turn on when REM floating.
Positive Logic Remote	Input current	_	_	_	0.5	mA	The input current of the REM
Control	OFF	-	0	-	1.5	V	Refer to -V <sub>in</sub>
	Output current	_	_	_	1.0	mA	The output current of the REM

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# **ZDR20-48BS3V3ST** DC-DC Converters

Input 18V-75V, Output 3.3V/6A,  $1in.\times1in$ . Standard Size

Output	Symbol	Min	Тур	Max	Unit	Conditions
Output Voltage	$V_{O1}$	3.25	3.30	3.35	Vdc	
Output Current	I <sub>O1,nom</sub>	0	-	6	A	
Output Voltage Adjust Range	$V_{trim}$	2.97	_	3.63	Vdc	
Line Regulation	$S_{V}$	-	-	±0.2	% V <sub>O</sub>	$V_{in}$ : 18 $\sim$ 75 $Vdc$ , $I_O$ =6 $A$
Load Regulation	$S_{I}$	-	-	±0.5	% V <sub>O</sub>	$V_{in}$ =48V, $I_O$ : 0A $\sim$ 6A
Output Over Current Protection Range	$I_{O,lim}$	110	-	170	% I <sub>O</sub>	V <sub>in</sub> =48V
Output Overshoot	$V_{TO}$	-	-	10	% V <sub>O</sub>	V <sub>in</sub> =48V, pure resistive load
Output Short-circuit Protection	automatic recovery					-

#### Continue

Ou	tput	Symbol	Min	Тур	Max	Unit	Conditions
	eak Ripple Noise	$\triangle V_{pp}$	-	-	75	mV	20MHz bandwidth
Rise	Time	T <sub>rise</sub>	ı	5	_	ms	V <sub>in</sub> =24V ,pure resistive load
Capacit	ive Load	Co	0	_	1000	μF	pure resistive load
Load	Recovery Time	$t_{\rm tr}$	_	200	_	μs	25%~50%~25% Io,nom or
Transient	Voltage Deviation	$\triangle V_{tr}$	_	±165	_	mV	50% ~75% ~50% Io,nom; 0.1A/μs

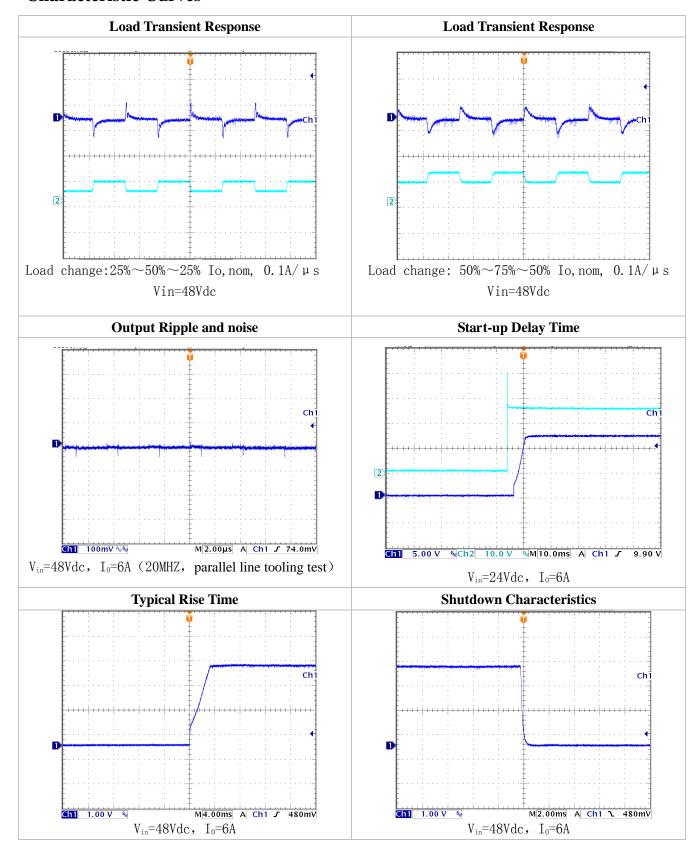
General	Symbol	Min	Тур	Max	Unit	Conditions
Efficiency	η	86.5	88	-	%	V <sub>in</sub> =48V, Io,nom
Switching Frequency	$f_s$	I	300	=	kHz	_
Isolation Resistance	R <sub>iso</sub>	50	ı	=	$M\Omega$	_
Relative Humidity		I	I	90%	RH%	Frost free
MTBF	_	1	$2 \times 10^{6}$	_	h	BELLCORE TR-332,
Isolation Voltage	$V_{iso}$	2250	-	-	Vdc	Input to output
Operating case Temperature		-40	ı	+105	$^{\circ}$	_
Storage Temperature	_	-55	I	+125	$^{\circ}$	_
Temperature Coefficient	$S_{\mathrm{T}}$			±0.02	%/°C	_
Hand Soldering	Maximum soldering Temperature $< 425^{\circ}\text{C}$ , and duration $< 5\text{s}$					
Wave Soldering	Maximum soldering Temperature $<$ 255 $^{\circ}$ C , and duration $<$ 10s					

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### ZDR20-48BS3V3ST DC-DC Converters

Input 18V-75V, Output 3.3V/6A, 1in.×1in. Standard Size

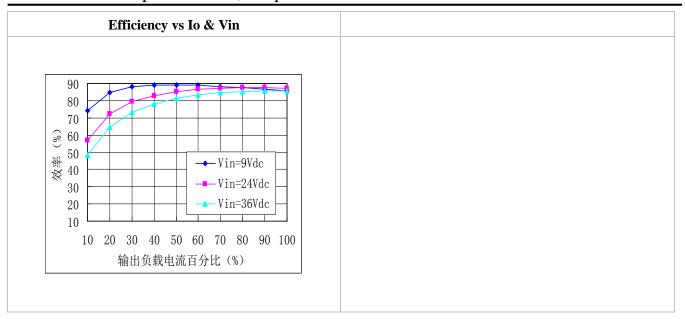
### **Characteristic Curves**



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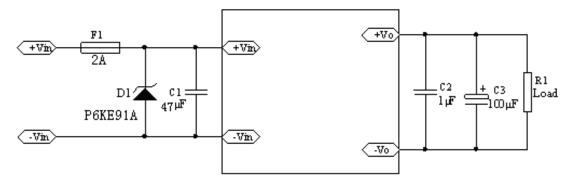
### ZDR20-48BS3V3ST DC-DC Converters

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# **Design Considerations**

#### **Basic Connection**



Notes: The basic connection indicates the basic requirements that the power module can provide rated output voltage and rated power only. Please refer the instruction followed for further information.

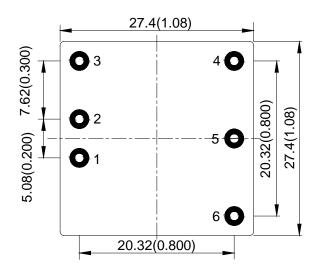
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### ZDR20-48BS3V3ST DC-DC Converters

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#### **Recommended Layout**



NO.	Recommendation & Notes					
Pad Design	TH package: Pad holes: 1.3mm, pad diameter including hole:3.5mm; SMT package: Pad Φ 2.0mm					
Installation	The metal heat sink faces upward					
direction	to make the hot air flow.					
Safaty	Isolated Converters, care to the					
Safety	spacing between input and output.					
	The Vin (-) and Vo (-) planes					
	should be placed under of the					
Electrical	converter separately. Avoid					
	routing sensitive signal or high					
	disturbance AC signal under the					
	converter.					

#### **Input Voltage Range**

The input voltage range of the DC/DC converter is 18V to  $75V_{\circ}$ . The input impedance of the converter looks like a negative resistor, which can interact with the reactance of the power bus (including any filter elements that have been added to the input of the converter), causes an unstable condition. Depending on the internal transformer's impedance, the external impedance usually should not exceed the 10% of the internal. So, the source impedance of the Power bus should be kept as low as possible.

The method to determine whether the impedance of the power bus too high or not is to decrease the converter's input voltage from higher to lower gradually, if the output voltage decreases (unstable sometime) with the lower input voltage, it will be considered the impedance too large. For further confirmation, one electrolytic capacitor can be paralleled to the converter pins after the converter shuts down (one  $1\mu F$  ceramic capacitor may be required to be paralleled with the electrolytic capacitor), if the output getting better, it will be sure that the impedance is too large.

#### **Remote Control**

Remote control can be offered by setting right control voltage level (or floating) to CNT pin. ZDR20-48S3V3T is provided with positive logic remote control. When the level is between 3.5V and 15V or be left floating, the converter will be turned on. When the level is less than 1.5V, the converter will be turned off. When low voltage level is applied, the output current of the REM is less than 1.0mA. When high voltage level  $(3.5V\sim15V)$  is applied, the input current of the REM is less than 0.5mA.

Due to the logic comparator is semiconductor integrated chip, they have low endurance to surge. Care should be taken to prevent REM from surge, A TVS should be used in some cases. When the pin is left floating, 2.0V-6.0V voltage appears on he pin.

In some applications, extra controls will be designed for the converter in user's PCB, such as output short circuit protection, over voltage protection, under voltage protection, and so on, remote control will give you help. The controls can be achieved by external circuit applied to the CNT pin.

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In some applications it is necessary to have a precise turn on and turn off level, or the level which can be received has a very narrow range, the aux. circuit will be required. Please contact us for more information.

#### **External Capacitance**

Unless special purpose (i.e. prolonging hold-up time, input impedance matching), the recommended input filter's capacitance ranges  $47\mu\text{F}-100\mu\text{F}$ , which not only offers a stable system, and reduces the cost, but also lessens the inrush current when the power supplies.

When larger capacitance is required, a circuit of suppressing the inrush current is recommended when the regulator start-up and a discharge circuit is recommended when the output dropped, ensuring the reliability and safety of other equipment in the system.

#### Thermal Consideration

The converters operate in a variety of thermal environments; however, sufficient cooling should be provided to ensure reliable operation of the unit. Heat is removed by conduction, convection and radiation to the surrounding environment. For the specified ambient temperature, user can increase airflow and change the size of heatsink to improve the heat dissipating for the module with baseplate; user can only increase airflow to improve the heat dissipating for the models without heat sink. Note that the natural convection condition means that airflow is 0.1 m/s.

#### **Safety Consideration**

The converter, as a component for the end user, should be installed into the equipment, and all the safety considerations are achieved under certain condition. It is required to meet safety requirements in system design. The converter output is considered SELV, and the expected input is considered TNV2, the primary to secondary is basic insulation to EN60950. The maximum operating temperature for PCB is 150 °C.

To avoid fire and be protected when short circuit occurred, it is recommended that a fast blow fuse with rating 2.5 to 3 times of converter's continuous input peak current is used at the input terminal.

#### **Series and Parallel Operation**

The converters should not be paralleled directly to increase power, but they can be paralleled each other through o-ring switches or diodes. Make sure that every converter's maximum load current should not exceed the rated current at anytime if they are paralleled without using external current sharing circuits. For the case that there is no external current sharing circuit, but power needs to be increased, please use Trim pin to adjust each converter's output voltage, to load the current as equal as possible in operating (When the load changes in a wide range, the method will not work).

The converters can operate in series. To prevent against start-up failure due to start up time difference, SBD with low voltage difference can be paralleled at the output pins. (SBD negative terminal connect to the positive pin of the output) for each converter.

#### **ESD Control**

The converters are processed and manufactured in an ESD controlled environment and supplied in conductive packaging to prevent ESD damage from occurring before or during shipping. It is essential that they are unpacked and handled using an ESD control procedures. Failure to do so affects the lifetime of the converter.

### **Quality Statement**

The converters are manufactured in accordance with ISO 9001 system requirements, and are monitored 100% by

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auto-testing system, 100% burn in. The warranty for the converters is 5-year.

### **Contact Information**

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